



BY THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/624,627
Filing Date July 21, 2003
Inventor..... Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Kennedy, Jennifer M.
Attorney's Docket No. MI22-2358
Title: Methods of Forming Semiconductor Constructions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56.

Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 OG 55). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

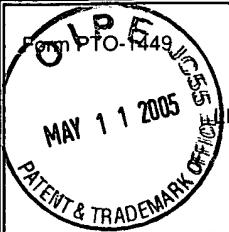
Dated: May 11, 2005

By: Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

05/16/2005 SMINASS1 00000031 10624627

01 FC:1806

180.00 OP

	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-2358	SERIAL NO. 10/624,627
	LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT: Luan C. Tran	
			FILING DATE July 21, 2003	GROUP 2812

U.S. PATENT DOCUMENTS

*Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,534,456	07/96	Yuan et al.			
	AB	5,970,352	10/99	Shiozawa et al.			
	AC	6,001,707	12/99	Lin et al.			
	AD	6,420,749	07/02	Divakaruni et al.			
	AE	6,187,624	02/01	Huang			
	AF	5,946,568	08/99	Hsiao et al.			
	AG	6,242,329	06/01	Huster et al.			
	AH	5,904,530	05/99	Shin			
	AI	US2002/0006693	01/02	Matsuda			

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AJ	JP04105328A	04/92	Japan				
	AK							
	AL							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

	AM		Wolf et al., <i>Silicon Processing for the VLSI Era</i> , Vol. 1: "Process Technology," Second Edition, Lattice Press 2000, pp. 12-13, 25-27.				
	AN		S.M. Sze, <i>VLSI Technology</i> , Second Edition, McGraw-Hill, 1988, pages 472-483				
	AO						

EXAMINER DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.